

Table of Contents

Preface	ix
I Part I	1
1 Network Programmability: A Historical Perspective	3
<i>W. Cerroni, S. Giordano</i>	
1.1 Introduction	3
1.2 Network Programmability	5
1.2.1 Programmable and Active Networks	5
1.2.2 Network control and data plane separation	6
1.2.3 OpenFlow and the rise of Software Defined Networking	7
1.2.4 Programmable data plane	9
1.3 Network Virtualization	10
1.3.1 Early concepts and link virtualization	11
1.3.2 Node virtualization and overlay networks	12
1.3.3 Network Function Virtualization	13
1.4 What's next	16
2 Software Defined Networking (SDN)	19
<i>S. Palazzo, G. M. Milotta, G. Morabito</i>	
2.1 Introduction	19
2.2 SDN: definitions and architecture	19
2.2.1 What is SDN?	20
2.2.2 SDN Architecture	23
2.2.3 OpenFlow	24
2.3 The three fundamental abstractions of SDN	25
2.3.1 Forwarding abstraction	26
2.3.2 State distribution abstraction	29
2.3.3 Specification abstraction, from SDNv1 to SDNv2	30
2.4 Security in SDN	34
2.4.1 SDN and security challenges and issues	34
2.4.2 Addressing security in SDN	37
2.5 Multitenancy and network slicing	38
2.6 SDN from theory to practice	40
2.6.1 What is Mininet?	40
2.6.2 Hands on Mininet	40

2.7	Conclusion	45
3	Network Function Virtualization (NFV)	49
	<i>S. Palazzo, C. Grasso, G. Schembra</i>	
3.1	Introduction	49
3.2	NFV in a nutshell	51
3.3	NFV Architecture	53
3.3.1	NFV Infrastructure (NFVI)	53
3.3.2	Service plane	55
3.3.3	NFV Management and Network Orchestration (MANO)	55
3.4	Virtualization: the enabler technology for network softwarization	56
3.4.1	VNF deployment inside Virtual Machines (VMs)	57
3.4.2	VNF deployment inside Containers	58
3.4.3	VNF deployment as Bare-metal Processes	59
3.4.4	VNF deployment through hybrid approaches	59
3.5	Challenges and open issues	59
3.5.1	Security challenges and possible solutions	60
3.5.2	Resource allocation	62
3.5.3	Emerging research challenges in resource allocation	65
3.6	An example of creation of a Network Service	66
	List of Terms and Abbreviations	77
4	The Programmable Data Plane	79
	<i>G. Bianchi, R. Bifulco</i>	
4.1	Introduction	79
4.2	The programmable data plane: a definition	81
4.3	Why the programmable data plane was needed?	82
4.4	Programmable switch architectures	84
4.5	Programmable data plane and smart NICs	90
4.6	Conclusion	92
II	Part II	95
5	Multi-level Orchestration of Micro-Services and Network Functions	97
	<i>R. Bolla, R. Bruschi, F. Davoli, C. Lombardo, J. F. Pajo</i>	
5.1	Introduction	97
5.2	Application-level orchestration	99
5.3	Overview of existing NFVO efforts and the role of the OSS	102
5.4	Mobile Edge integration	104
5.5	Dynamic monitoring and reconfiguration	106
5.6	Conclusion	110
6	Network Slicing	115
	<i>C. Raffaelli, P. Monti, F. Tonini</i>	
6.1	Introduction	115
6.1.1	Background, concepts and motivations	115

6.1.2	Business opportunities	117
6.1.3	Standardization bodies and activities	118
6.2	Slice Design	119
6.2.1	Quality of service requirements	119
6.2.2	Problem definition and objectives	120
6.2.3	Network slice optimization and performance	122
6.3	Slice Deployment	123
6.3.1	Main software components	123
6.3.2	End-to-end network slicing	124
6.3.3	Life cycle management	125
6.4	Challenges in Slice Design and Operation	127
6.4.1	Slice automation	127
6.4.2	Attacks, security, vulnerability	128
6.4.3	Differentiated Reliability	128
6.5	Conclusion	130
7	VNF Placement and Artificial Intelligence (AI)-based Resource Optimization	133
	<i>V. Eramo, A. Cianfrani, F. G. Lavacca, M. Polverini, T. Catena</i>	
7.1	Introduction	133
7.2	The ETSI NFV Reference Scenario	134
7.2.1	The Multi-Cloud NFV scenario	134
7.2.2	The VNF Placement and Chaining problem	135
7.3	VNF Placement Algorithms in Static Traffic Scenario	136
7.4	VNF Placement Algorithms in Dynamic Traffic Scenario	138
7.4.1	Under-provisioning and Over-provisioning costs in prediction-based NFV reconfiguration algorithms	140
7.4.2	Artificial Intelligence (AI)-based NFV Reconfiguration Algorithm	141
7.4.3	LSTM Prediction Algorithm	142
7.4.4	A Case Study	145
7.5	Conclusion	147
8	Service Function Chaining	151
	<i>D. Borsatti, F. Callegati, G. Davoli</i>	
8.1	Introduction	151
8.1.1	Network Services and the Service Function Chain	151
8.1.2	SFC may be Dynamic	153
8.1.3	SFC is not Simple Forwarding	153
8.2	Standardization activities	153
8.2.1	IETF	154
8.2.2	The ONF view	156
8.2.3	ETSI NFV-MANO	156
8.3	Examples of SFC implementation	156
8.3.1	Implementing SFC with NSH and OpenFlow	157
8.3.2	SFC implementation with MPLS and/or Segment Routing	160
8.4	Orchestrating Service Function Chaining	163
8.4.1	High level orchestration with OSM	164

8.4.2	The OpenStack case	164
9	Intent-based Networking	169
	<i>M. Gharbaoui, C. Contoli, L. M. Contreras, B. Martini, W. Cerroni</i>	
9.1	Introduction to Intent-based Networking	169
9.2	Definitions and Standardization Efforts	172
9.3	Network Intent	174
9.3.1	Transport Network Intent	174
9.3.2	SDN Network Intent	175
9.4	Service Chaining Intent	177
9.5	Slice Intent	180
9.6	Conclusion and Open Issues	181
10	Security Implications, Issues and Approaches in Programmable Networks	185
	<i>R. Bolla, A. Carrega, F. Davoli, M. Repetto, O. R. Sanchez</i>	
10.1	Introduction	185
10.2	Security challenges in softwarized networks	187
10.3	Beyond the security perimeter	189
10.4	Architectural service-centric framework	191
10.5	Current trends in cybersecurity leveraging network programmability	193
10.5.1	Security Manager	194
10.5.2	Identity Management	195
10.5.3	Local agents	196
10.5.4	Security Dashboard	197
10.6	Cyber-range approach to test security of virtualized network environments	197
10.7	Conclusion	199
11	Software Acceleration Techniques for High-speed Programmable Networks	203
	<i>L. Linguaglossa</i>	
11.1	Introduction	203
11.2	High-speed software networking on COTS servers	204
11.2.1	COTS subsystems	205
11.2.2	Components of the packet-processing workflow	207
11.3	Software acceleration techniques	209
11.3.1	RX/TX	209
11.3.2	Memory optimization	211
11.3.3	Threading and coding	212
11.3.4	NIC-assisted acceleration	214
11.3.5	CPU-assisted acceleration	215
11.4	Summary	216
12	5G Radio Access Network Virtualization	223
	<i>J. C. Borromeo, K. Kondepu, L. Valcarenghi</i>	
12.1	Introduction	223
12.2	5G RAN Architecture Overview	224

12.2.1	DU/CU functional split options and requirements	225
12.2.2	NFV and vRAN	226
12.3	Experimental Setup and Evaluation Parameters	227
12.4	Experimental Results	229
12.5	Hardware Acceleration	230
12.6	Conclusion	232
13	Optical Networks Evolution: From Programmability to Machine Learning and Spatial Division Multiplexing	237
	<i>F. Cugini, M. Tornatore, N. Sambo, C. Rottondi, A. Marotta</i>	
13.1	Introduction	237
13.2	Programmable optical data plane	238
13.3	Disaggregation in optical networks	240
13.3.1	Partial Disaggregation	241
13.3.2	Full Disaggregation	241
13.3.3	Partial vs. Full Disaggregation	242
13.4	SDN Control of optical networks	243
13.5	ML-empowered optical network automation	244
13.6	Spatial Division Multiplexing	247
13.6.1	CNIT FIBERS Testbed for SDM	248
14	Software Defined Wide Area Networking (SD-WAN)	253
	<i>S. Troia, G. Maier</i>	
14.1	Introduction	253
14.2	Background	255
14.2.1	Traditional approaches	255
14.2.2	SD-WAN	256
14.3	Active and passive monitoring	258
14.3.1	Active monitoring	259
14.3.2	Passive monitoring	259
14.4	Related work	260
14.5	SD-WAN proposed model	262
14.5.1	Architecture	262
14.5.2	Active monitoring module	263
14.5.3	Passive monitoring module	264
14.5.4	Path switching module	266
14.6	Experiments and results	267
14.6.1	Active monitoring performance	267
14.6.2	Passive monitoring performance	267
14.7	Conclusion and discussion	268
15	Segment Routing over IPv6 (SRv6)	271
	<i>F. Lombardo, C. Scarpitta, A. Mayer, P. Lungaroni, P. L. Ventre, S. Salsano, A. Abdelsalam</i>	
15.1	Introduction on SRv6	271
15.1.1	SRv6 data plane	272
15.1.2	SRv6 Network Programming Model	273
15.1.3	SRv6 control plane	275

15.1.4	Scenarios and use cases	275
15.2	Implementation and open source tools for experiments	275
15.2.1	The ROSE project	275
15.2.2	The ROSE SRv6 Virtual Machine	276
15.2.3	The Big Data infrastructure	282
15.2.4	Use cases for the emulation platform	283
16	The P4 Ecosystem for Network Programmability	289
	<i>D. Moro, D. Sanvito, C. Cascone, G. Verticale, A. Capone</i>	
16.1	Abstracting a switch through a language	289
16.1.1	Dealing with hardware heterogeneity	290
16.2	P4 Language and P4Runtime Interface	292
16.2.1	P4 Program	292
16.2.2	Controlling switches at run-time with P4Runtime	296
16.3	Use Cases Enabled by P4	297
16.3.1	Testing P4 Programs	298
16.3.2	Software Defined Broadband Network Gateway (SD-BNG)	298
16.3.3	In-band Network Telemetry (INT)	299
16.3.4	In-Network Computing Paradigm	300
16.4	Conclusion	302
17	Extended Berkeley Packet Filter	307
	<i>S. Miano, F. Rizzo</i>	
17.1	Introduction	307
17.1.1	Classical BPF (cBPF)	308
17.2	Extended BPF (eBPF)	309
17.2.1	The “universal” in-kernel vCPU	309
17.2.2	The eBPF subsystem: main features	310
17.2.3	Technology Pillars	312
17.3	eBPF for tracing	314
17.4	In-kernel networking with eBPF	314
17.4.1	eXpress Data Path (XDP)	316
17.4.2	Traffic Control (TC)	317
17.4.3	Socket Layer	318
17.5	Creating Complex Network Function with eBPF	318
17.5.1	Polycube	319
17.6	Conclusion	320